

On the Composition of Macro Instructions. Part III ¹

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Summary. This article is a continuation of [27] and [2]. First, we recast the semantics of the macro composition in more convenient terms. Then, we introduce terminology and basic properties of macros constructed out of single instructions of $\mathbf{SCM}_{\text{FSA}}$. We give the complete semantics of composing a macro instruction with an instruction and for composing two machine instructions (this is also done in terms of macros). The introduced terminology is tested on the simple example of a macro for swapping two integer locations.

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The papers [23], [31], [15], [4], [29], [18], [32], [10], [11], [5], [24], [9], [30], [13], [3], [21], [8], [14], [12], [22], [16], [17], [26], [6], [20], [7], [28], [25], [27], [19], and [1] provide the notation and terminology for this paper.

1. PRELIMINARIES

For simplicity we adopt the following rules: i will denote an instruction of $\mathbf{SCM}_{\text{FSA}}$, a , b will denote integer locations, f will denote a finite sequence location, l will denote an instruction-location of $\mathbf{SCM}_{\text{FSA}}$, and s , s_1 , s_2 will denote states of $\mathbf{SCM}_{\text{FSA}}$.

The following propositions are true:

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- (1) Let I be a keeping 0 parahalting macro instruction and let J be a parahalting macro instruction. Then $(\text{IExec}(I;J,s))(a) = (\text{IExec}(J,\text{IExec}(I,s)))(a)$.
- (2) Let I be a keeping 0 parahalting macro instruction and let J be a parahalting macro instruction. Then $(\text{IExec}(I;J,s))(f) = (\text{IExec}(J,\text{IExec}(I,s)))(f)$.

2. PARAHALTING AND KEEPING 0 MACRO INSTRUCTIONS

Let i be an instruction of $\mathbf{SCM}_{\text{FSA}}$. We say that i is parahalting if and only if:

(Def. 1) $\text{Macro}(i)$ is parahalting.

We say that i is keeping 0 if and only if:

(Def. 2) $\text{Macro}(i)$ is keeping 0.

Let us observe that $\mathbf{halts}_{\mathbf{SCM}_{\text{FSA}}}$ is keeping 0 and parahalting.

Let us note that there exists an instruction of $\mathbf{SCM}_{\text{FSA}}$ which is keeping 0 and parahalting.

Let i be a parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$. Observe that $\text{Macro}(i)$ is parahalting.

Let i be a keeping 0 instruction of $\mathbf{SCM}_{\text{FSA}}$. Observe that $\text{Macro}(i)$ is keeping 0.

Let a, b be integer locations. One can check the following observations:

- * $a:=b$ is parahalting,
- * $\text{AddTo}(a,b)$ is parahalting,
- * $\text{SubFrom}(a,b)$ is parahalting,
- * $\text{MultBy}(a,b)$ is parahalting, and
- * $\text{Divide}(a,b)$ is parahalting.

Let f be a finite sequence location. Note that $b:=f_a$ is parahalting and $f_a:=b$ is parahalting and keeping 0.

Let a be an integer location and let f be a finite sequence location. Note that $a:=\text{len } f$ is parahalting and $f:=\underbrace{(0, \dots, 0)}_a$ is parahalting and keeping 0.

Let a be a read-write integer location and let b be an integer location. One can verify the following observations:

- * $a:=b$ is keeping 0,
- * $\text{AddTo}(a,b)$ is keeping 0,
- * $\text{SubFrom}(a,b)$ is keeping 0, and
- * $\text{MultBy}(a,b)$ is keeping 0.

Let a, b be read-write integer locations. Note that $\text{Divide}(a,b)$ is keeping 0.

Let a be an integer location, let f be a finite sequence location, and let b be a read-write integer location. Observe that $b:=f_a$ is keeping 0.

Let f be a finite sequence location and let b be a read-write integer location. Observe that $b := \text{len } f$ is keeping 0.

Let i be a parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$ and let J be a parahalting macro instruction. One can verify that $i;J$ is parahalting.

Let I be a parahalting macro instruction and let j be a parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$. Note that $I;j$ is parahalting.

Let i be a parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$ and let j be a parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$. Note that $i;j$ is parahalting.

Let i be a keeping 0 instruction of $\mathbf{SCM}_{\text{FSA}}$ and let J be a keeping 0 macro instruction. Observe that $i;J$ is keeping 0.

Let I be a keeping 0 macro instruction and let j be a keeping 0 instruction of $\mathbf{SCM}_{\text{FSA}}$. One can check that $I;j$ is keeping 0.

Let i, j be keeping 0 instructions of $\mathbf{SCM}_{\text{FSA}}$. One can check that $i;j$ is keeping 0.

3. SEMANTICS OF COMPOSITIONS

Let s be a state of $\mathbf{SCM}_{\text{FSA}}$. The functor $\text{Initialize}(s)$ yielding a state of $\mathbf{SCM}_{\text{FSA}}$ is defined as follows:

(Def. 3) $\text{Initialize}(s) = s + \cdot (\text{intloc}(0) \mapsto 1) + \cdot \text{Start-At}(\text{insloc}(0))$.

The following propositions are true:

- (3) (i) $\mathbf{IC}_{\text{Initialize}(s)} = \text{insloc}(0)$,
- (ii) $(\text{Initialize}(s))(\text{intloc}(0)) = 1$,
- (iii) for every read-write integer location a holds $(\text{Initialize}(s))(a) = s(a)$,
- (iv) for every f holds $(\text{Initialize}(s))(f) = s(f)$, and
- (v) for every l holds $(\text{Initialize}(s))(l) = s(l)$.
- (4) s_1 and s_2 are equal outside the instruction locations of $\mathbf{SCM}_{\text{FSA}}$ iff $s_1 \upharpoonright (\text{Int-Locations} \cup \text{FinSeq-Locations} \cup \{\mathbf{IC}_{\mathbf{SCM}_{\text{FSA}}}\}) = s_2 \upharpoonright (\text{Int-Locations} \cup \text{FinSeq-Locations} \cup \{\mathbf{IC}_{\mathbf{SCM}_{\text{FSA}}}\})$.
- (5) If $s_1 \upharpoonright (\text{Int-Locations} \cup \text{FinSeq-Locations}) = s_2 \upharpoonright (\text{Int-Locations} \cup \text{FinSeq-Locations})$, then $\text{Exec}(i, s_1) \upharpoonright (\text{Int-Locations} \cup \text{FinSeq-Locations}) = \text{Exec}(i, s_2) \upharpoonright (\text{Int-Locations} \cup \text{FinSeq-Locations})$.
- (6) For every parahalting instruction i of $\mathbf{SCM}_{\text{FSA}}$ holds $\text{Exec}(i, \text{Initialize}(s)) = \mathbf{IExec}(\text{Macro}(i), s)$.
- (7) Let I be a keeping 0 parahalting macro instruction and let j be a parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$. Then $(\mathbf{IExec}(I;j, s))(a) = (\text{Exec}(j, \mathbf{IExec}(I, s)))(a)$.
- (8) Let I be a keeping 0 parahalting macro instruction and let j be a parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$. Then $(\mathbf{IExec}(I;j, s))(f) = (\text{Exec}(j, \mathbf{IExec}(I, s)))(f)$.

- (9) Let i be a keeping 0 parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$ and let j be a parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$. Then $(\text{IExec}(i;j,s))(a) = (\text{Exec}(j, \text{Exec}(i, \text{Initialize}(s))))(a)$.
- (10) Let i be a keeping 0 parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$ and let j be a parahalting instruction of $\mathbf{SCM}_{\text{FSA}}$. Then $(\text{IExec}(i;j,s))(f) = (\text{Exec}(j, \text{Exec}(i, \text{Initialize}(s))))(f)$.

4. AN EXAMPLE: SWAP

Let a, b be integer locations. The functor $\text{swap}(a, b)$ yields a macro instruction and is defined as follows:

(Def. 4) $\text{swap}(a, b) = (\text{FirstNotUsed}(\text{Macro}(a:=b)):=a);(a:=b);(b:=\text{FirstNotUsed}(\text{Macro}(a:=b)))$.

Let a, b be integer locations. Observe that $\text{swap}(a, b)$ is parahalting.

Let a, b be read-write integer locations. Note that $\text{swap}(a, b)$ is keeping 0.

We now state two propositions:

- (11) For all read-write integer locations a, b holds $(\text{IExec}(\text{swap}(a, b), s))(a) = s(b)$ and $(\text{IExec}(\text{swap}(a, b), s))(b) = s(a)$.
- (12) $\text{UsedInt}^* \text{Loc}(\text{swap}(a, b)) = \emptyset$.

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