

# Memory Handling for $\text{SCM}_{\text{FSA}}$ <sup>1</sup>

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**Summary.** We introduce some terminology for reasoning about memory used in programs in general and in macro instructions (introduced in [26]) in particular. The usage of integer locations and of finite sequence locations by a program is treated separately. We define some functors for selecting memory locations needed for local (temporary) variables in macro instructions. Some semantic properties of the introduced notions are given in terms of executions of macro instructions.

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The articles [21], [31], [19], [12], [30], [22], [14], [2], [28], [15], [20], [6], [13], [1], [3], [17], [11], [4], [7], [29], [32], [8], [9], [10], [5], [16], [25], [18], [27], [23], [24], and [26] provide the terminology and notation for this paper.

## 1. PRELIMINARIES

One can prove the following three propositions:

- (1) For all sets  $x, y, a$  and for every function  $f$  such that  $f(x) = f(y)$  holds  $f(a) = (f \cdot (\text{id}_{\text{dom } f} + \cdot (x, y)))(a)$ .
- (2) For all sets  $x, y$  and for every function  $f$  such that if  $x \in \text{dom } f$ , then  $y \in \text{dom } f$  and  $f(x) = f(y)$  holds  $f = f \cdot (\text{id}_{\text{dom } f} + \cdot (x, y))$ .
- (3) For all sets  $A, B$  and for every function  $f$  from  $A$  into  $B$  holds  $\text{dom } f \subseteq A$ .

Let  $A$  be a finite set and let  $B$  be a set. Note that every function from  $A$  into  $B$  is finite.

Let  $A$  be a finite set, let  $B$  be a set, and let  $f$  be a function from  $A$  into  $\text{Fin } B$ . Observe that  $\text{Union } f$  is finite.

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In the sequel  $N$  will be a non empty set with non empty elements.

The following proposition is true

- (4) Let  $S$  be a definite AMI over  $N$  and let  $p$  be a programmed finite partial state of  $S$ . Then  $\text{rng } p \subseteq$  the instructions of  $S$ .

Let us mention that Int-Locations is non empty.

Let us mention that FinSeq-Locations is non empty.

## 2. UNIQUENESS OF INSTRUCTION COMPONENTS

For simplicity we adopt the following rules:  $a, b, c, a_1, a_2, b_1, b_2$  will be integer locations,  $l, l_1, l_2$  will be instructions-locations of  $\mathbf{SCM}_{\text{FSA}}$ ,  $f, f_1, f_2$  will be finite sequence locations, and  $i, j$  will be instructions of  $\mathbf{SCM}_{\text{FSA}}$ .

The following propositions are true:

- (5) If  $a_1 := b_1 = a_2 := b_2$ , then  $a_1 = a_2$  and  $b_1 = b_2$ .  
(6) If  $\text{AddTo}(a_1, b_1) = \text{AddTo}(a_2, b_2)$ , then  $a_1 = a_2$  and  $b_1 = b_2$ .  
(7) If  $\text{SubFrom}(a_1, b_1) = \text{SubFrom}(a_2, b_2)$ , then  $a_1 = a_2$  and  $b_1 = b_2$ .  
(8) If  $\text{MultBy}(a_1, b_1) = \text{MultBy}(a_2, b_2)$ , then  $a_1 = a_2$  and  $b_1 = b_2$ .  
(9) If  $\text{Divide}(a_1, b_1) = \text{Divide}(a_2, b_2)$ , then  $a_1 = a_2$  and  $b_1 = b_2$ .  
(10) If  $\text{goto } l_1 = \text{goto } l_2$ , then  $l_1 = l_2$ .  
(11) If  $\text{if } a_1 = 0 \text{ goto } l_1 = \text{if } a_2 = 0 \text{ goto } l_2$ , then  $a_1 = a_2$  and  $l_1 = l_2$ .  
(12) If  $\text{if } a_1 > 0 \text{ goto } l_1 = \text{if } a_2 > 0 \text{ goto } l_2$ , then  $a_1 = a_2$  and  $l_1 = l_2$ .  
(13) If  $b_1 := f_{1a_1} = b_2 := f_{2a_2}$ , then  $a_1 = a_2$  and  $b_1 = b_2$  and  $f_1 = f_2$ .  
(14) If  $f_{1a_1} := b_1 = f_{2a_2} := b_2$ , then  $a_1 = a_2$  and  $b_1 = b_2$  and  $f_1 = f_2$ .  
(15) If  $a_1 := \text{len } f_1 = a_2 := \text{len } f_2$ , then  $a_1 = a_2$  and  $f_1 = f_2$ .  
(16) If  $f_1 := \underbrace{(0, \dots, 0)}_{a_1} = f_2 := \underbrace{(0, \dots, 0)}_{a_2}$ , then  $a_1 = a_2$  and  $f_1 = f_2$ .

## 3. INTEGER LOCATIONS USED IN MACROS

Let  $i$  be an instruction of  $\mathbf{SCM}_{\text{FSA}}$ . The functor  $\text{UsedIntLoc}(i)$  yields an element of Fin Int-Locations and is defined as follows:

- (Def. 1) (i) There exist integer locations  $a, b$  such that  $i = a := b$  or  $i = \text{AddTo}(a, b)$  or  $i = \text{SubFrom}(a, b)$  or  $i = \text{MultBy}(a, b)$  or  $i = \text{Divide}(a, b)$  but  $\text{UsedIntLoc}(i) = \{a, b\}$  if  $\text{InsCode}(i) \in \{1, 2, 3, 4, 5\}$ ,  
(ii) there exists an integer location  $a$  and there exists an instruction-location  $l$  of  $\mathbf{SCM}_{\text{FSA}}$  such that  $i = \text{if } a = 0 \text{ goto } l$  or  $i = \text{if } a > 0 \text{ goto } l$  but  $\text{UsedIntLoc}(i) = \{a\}$  if  $\text{InsCode}(i) = 7$  or  $\text{InsCode}(i) = 8$ ,  
(iii) there exist integer locations  $a, b$  and there exists a finite sequence location  $f$  such that  $i = b := f_a$  or  $i = f_a := b$  but  $\text{UsedIntLoc}(i) = \{a, b\}$  if  $\text{InsCode}(i) = 9$  or  $\text{InsCode}(i) = 10$ ,

- (iv) there exists an integer location  $a$  and there exists a finite sequence location  $f$  such that  $i = a := \text{len } f$  or  $i = f := \underbrace{\langle 0, \dots, 0 \rangle}_a$  but  $\text{UsedIntLoc}(i) = \{a\}$  if  $\text{InsCode}(i) = 11$  or  $\text{InsCode}(i) = 12$ ,
- (v)  $\text{UsedIntLoc}(i) = \emptyset$ , otherwise.

Next we state several propositions:

- (17)  $\text{UsedIntLoc}(\mathbf{halts}_{\mathbf{SCM}_{\text{FSA}}}) = \emptyset$ .
- (18) If  $i = a := b$  or  $i = \text{AddTo}(a, b)$  or  $i = \text{SubFrom}(a, b)$  or  $i = \text{MultBy}(a, b)$  or  $i = \text{Divide}(a, b)$ , then  $\text{UsedIntLoc}(i) = \{a, b\}$ .
- (19)  $\text{UsedIntLoc}(\text{goto } l) = \emptyset$ .
- (20) If  $i = \mathbf{if } a = 0 \mathbf{ goto } l$  or  $i = \mathbf{if } a > 0 \mathbf{ goto } l$ , then  $\text{UsedIntLoc}(i) = \{a\}$ .
- (21) If  $i = b := f_a$  or  $i = f_a := b$ , then  $\text{UsedIntLoc}(i) = \{a, b\}$ .
- (22) If  $i = a := \text{len } f$  or  $i = f := \underbrace{\langle 0, \dots, 0 \rangle}_a$ , then  $\text{UsedIntLoc}(i) = \{a\}$ .

Let  $p$  be a programmed finite partial state of  $\mathbf{SCM}_{\text{FSA}}$ . The functor

$\text{UsedIntLoc}(p)$  yields a subset of Int-Locations and is defined by the condition (Def. 2).

- (Def. 2) There exists a function  $U_1$  from the instructions of  $\mathbf{SCM}_{\text{FSA}}$  into FinInt-Locations such that for every instruction  $i$  of  $\mathbf{SCM}_{\text{FSA}}$  holds  $U_1(i) = \text{UsedIntLoc}(i)$  and  $\text{UsedIntLoc}(p) = \text{Union}(U_1 \cdot p)$ .

Let  $p$  be a programmed finite partial state of  $\mathbf{SCM}_{\text{FSA}}$ . Note that  $\text{UsedIntLoc}(p)$  is finite.

We follow the rules:  $p, r$  denote programmed finite partial states of  $\mathbf{SCM}_{\text{FSA}}$ ,  $I, J$  denote macro instructions, and  $k, m, n$  denote natural numbers.

Next we state a number of propositions:

- (23) If  $i \in \text{rng } p$ , then  $\text{UsedIntLoc}(i) \subseteq \text{UsedIntLoc}(p)$ .
- (24)  $\text{UsedIntLoc}(p+r) \subseteq \text{UsedIntLoc}(p) \cup \text{UsedIntLoc}(r)$ .
- (25) If  $\text{dom } p$  misses  $\text{dom } r$ , then  $\text{UsedIntLoc}(p+r) = \text{UsedIntLoc}(p) \cup \text{UsedIntLoc}(r)$ .
- (26)  $\text{UsedIntLoc}(p) = \text{UsedIntLoc}(\text{Shift}(p, k))$ .
- (27)  $\text{UsedIntLoc}(i) = \text{UsedIntLoc}(\text{IncAddr}(i, k))$ .
- (28)  $\text{UsedIntLoc}(p) = \text{UsedIntLoc}(\text{IncAddr}(p, k))$ .
- (29)  $\text{UsedIntLoc}(I) = \text{UsedIntLoc}(\text{ProgramPart}(\text{Relocated}(I, k)))$ .
- (30)  $\text{UsedIntLoc}(I) = \text{UsedIntLoc}(\text{Directed}(I))$ .
- (31)  $\text{UsedIntLoc}(I;J) = \text{UsedIntLoc}(I) \cup \text{UsedIntLoc}(J)$ .
- (32)  $\text{UsedIntLoc}(\text{Macro}(i)) = \text{UsedIntLoc}(i)$ .
- (33)  $\text{UsedIntLoc}(i;J) = \text{UsedIntLoc}(i) \cup \text{UsedIntLoc}(J)$ .
- (34)  $\text{UsedIntLoc}(I;j) = \text{UsedIntLoc}(I) \cup \text{UsedIntLoc}(j)$ .
- (35)  $\text{UsedIntLoc}(i;j) = \text{UsedIntLoc}(i) \cup \text{UsedIntLoc}(j)$ .

## 4. FINITE SEQUENCE LOCATIONS USED IN MACROS

Let  $i$  be an instruction of  $\mathbf{SCM}_{\text{FSA}}$ . The functor  $\text{UsedInt}^* \text{Loc}(i)$  yielding an element of  $\text{Fin FinSeq-Locations}$  is defined by:

- (Def. 3) (i) There exist integer locations  $a, b$  and there exists a finite sequence location  $f$  such that  $i = b := f_a$  or  $i = f_a := b$  but  $\text{UsedInt}^* \text{Loc}(i) = \{f\}$  if  $\text{InsCode}(i) = 9$  or  $\text{InsCode}(i) = 10$ ,
- (ii) there exists an integer location  $a$  and there exists a finite sequence location  $f$  such that  $i = a := \text{len } f$  or  $i = f := \underbrace{\langle 0, \dots, 0 \rangle}_a$  but  $\text{UsedInt}^* \text{Loc}(i) = \{f\}$  if  $\text{InsCode}(i) = 11$  or  $\text{InsCode}(i) = 12$ ,
- (iii)  $\text{UsedInt}^* \text{Loc}(i) = \emptyset$ , otherwise.

One can prove the following propositions:

- (36) If  $i = \mathbf{halt}_{\mathbf{SCM}_{\text{FSA}}}$  or  $i = a := b$  or  $i = \mathbf{AddTo}(a, b)$  or  $i = \mathbf{SubFrom}(a, b)$  or  $i = \mathbf{MultBy}(a, b)$  or  $i = \mathbf{Divide}(a, b)$  or  $i = \mathbf{goto } l$  or  $i = \mathbf{if } a = 0 \mathbf{ goto } l$  or  $i = \mathbf{if } a > 0 \mathbf{ goto } l$ , then  $\text{UsedInt}^* \text{Loc}(i) = \emptyset$ .
- (37) If  $i = b := f_a$  or  $i = f_a := b$ , then  $\text{UsedInt}^* \text{Loc}(i) = \{f\}$ .
- (38) If  $i = a := \text{len } f$  or  $i = f := \underbrace{\langle 0, \dots, 0 \rangle}_a$ , then  $\text{UsedInt}^* \text{Loc}(i) = \{f\}$ .

Let  $p$  be a programmed finite partial state of  $\mathbf{SCM}_{\text{FSA}}$ . The functor  $\text{UsedInt}^* \text{Loc}(p)$  yields a subset of  $\text{FinSeq-Locations}$  and is defined by the condition (Def. 4).

- (Def. 4) There exists a function  $U_1$  from the instructions of  $\mathbf{SCM}_{\text{FSA}}$  into  $\text{Fin FinSeq-Locations}$  such that for every instruction  $i$  of  $\mathbf{SCM}_{\text{FSA}}$  holds  $U_1(i) = \text{UsedInt}^* \text{Loc}(i)$  and  $\text{UsedInt}^* \text{Loc}(p) = \text{Union}(U_1 \cdot p)$ .

Let  $p$  be a programmed finite partial state of  $\mathbf{SCM}_{\text{FSA}}$ . Note that  $\text{UsedInt}^* \text{Loc}(p)$  is finite.

The following propositions are true:

- (39) If  $i \in \text{rng } p$ , then  $\text{UsedInt}^* \text{Loc}(i) \subseteq \text{UsedInt}^* \text{Loc}(p)$ .
- (40)  $\text{UsedInt}^* \text{Loc}(p + \cdot r) \subseteq \text{UsedInt}^* \text{Loc}(p) \cup \text{UsedInt}^* \text{Loc}(r)$ .
- (41) If  $\text{dom } p$  misses  $\text{dom } r$ , then  $\text{UsedInt}^* \text{Loc}(p + \cdot r) = \text{UsedInt}^* \text{Loc}(p) \cup \text{UsedInt}^* \text{Loc}(r)$ .
- (42)  $\text{UsedInt}^* \text{Loc}(p) = \text{UsedInt}^* \text{Loc}(\text{Shift}(p, k))$ .
- (43)  $\text{UsedInt}^* \text{Loc}(i) = \text{UsedInt}^* \text{Loc}(\text{IncAddr}(i, k))$ .
- (44)  $\text{UsedInt}^* \text{Loc}(p) = \text{UsedInt}^* \text{Loc}(\text{IncAddr}(p, k))$ .
- (45)  $\text{UsedInt}^* \text{Loc}(I) = \text{UsedInt}^* \text{Loc}(\text{ProgramPart}(\text{Relocated}(I, k)))$ .
- (46)  $\text{UsedInt}^* \text{Loc}(I) = \text{UsedInt}^* \text{Loc}(\text{Directed}(I))$ .
- (47)  $\text{UsedInt}^* \text{Loc}(I; J) = \text{UsedInt}^* \text{Loc}(I) \cup \text{UsedInt}^* \text{Loc}(J)$ .
- (48)  $\text{UsedInt}^* \text{Loc}(\text{Macro}(i)) = \text{UsedInt}^* \text{Loc}(i)$ .
- (49)  $\text{UsedInt}^* \text{Loc}(i; J) = \text{UsedInt}^* \text{Loc}(i) \cup \text{UsedInt}^* \text{Loc}(J)$ .
- (50)  $\text{UsedInt}^* \text{Loc}(I; j) = \text{UsedInt}^* \text{Loc}(I) \cup \text{UsedInt}^* \text{Loc}(j)$ .

$$(51) \quad \text{UsedInt}^* \text{Loc}(i;j) = \text{UsedInt}^* \text{Loc}(i) \cup \text{UsedInt}^* \text{Loc}(j).$$

## 5. CHOOSING AN INTEGER LOCATION NOT USED IN A PROGRAM

Let  $I_1$  be an integer location. We say that  $I_1$  is read-only if and only if:

$$(\text{Def. 5}) \quad I_1 = \text{intloc}(0).$$

We introduce  $I_1$  is read-write as an antonym of  $I_1$  is read-only.

Let us observe that  $\text{intloc}(0)$  is read-only.

One can check that there exists an integer location which is read-write.

In the sequel  $L$  will be a finite subset of Int-Locations.

Let  $L$  be a finite subset of Int-Locations. The functor  $\text{FirstNotIn}(L)$  yields an integer location and is defined by:

$$(\text{Def. 6}) \quad \text{There exists a non empty subset } s_1 \text{ of } \mathbb{N} \text{ such that } \text{FirstNotIn}(L) = \text{intloc}(\min s_1) \text{ and } s_1 = \{k : k \text{ ranges over natural numbers, } \text{intloc}(k) \notin L\}.$$

Next we state two propositions:

$$(52) \quad \text{FirstNotIn}(L) \notin L.$$

$$(53) \quad \text{If } \text{FirstNotIn}(L) = \text{intloc}(m) \text{ and } \text{intloc}(n) \notin L, \text{ then } m \leq n.$$

Let  $p$  be a programmed finite partial state of  $\mathbf{SCM}_{\text{FSA}}$ . The functor  $\text{FirstNotUsed}(p)$  yields an integer location and is defined by:

$$(\text{Def. 7}) \quad \text{There exists a finite subset } s_2 \text{ of Int-Locations such that } s_2 = \text{UsedIntLoc}(p) \cup \{\text{intloc}(0)\} \text{ and } \text{FirstNotUsed}(p) = \text{FirstNotIn}(s_2).$$

Let  $p$  be a programmed finite partial state of  $\mathbf{SCM}_{\text{FSA}}$ . Observe that  $\text{FirstNotUsed}(p)$  is read-write.

We now state several propositions:

$$(54) \quad \text{FirstNotUsed}(p) \notin \text{UsedIntLoc}(p).$$

$$(55) \quad \text{If } a:=b \in \text{rng } p \text{ or } \text{AddTo}(a,b) \in \text{rng } p \text{ or } \text{SubFrom}(a,b) \in \text{rng } p \text{ or } \text{MultBy}(a,b) \in \text{rng } p \text{ or } \text{Divide}(a,b) \in \text{rng } p, \text{ then } \text{FirstNotUsed}(p) \neq a \text{ and } \text{FirstNotUsed}(p) \neq b.$$

$$(56) \quad \text{If } \mathbf{if } a = 0 \mathbf{ goto } l \in \text{rng } p \text{ or } \mathbf{if } a > 0 \mathbf{ goto } l \in \text{rng } p, \text{ then } \text{FirstNotUsed}(p) \neq a.$$

$$(57) \quad \text{If } b:=f_a \in \text{rng } p \text{ or } f_a:=b \in \text{rng } p, \text{ then } \text{FirstNotUsed}(p) \neq a \text{ and } \text{FirstNotUsed}(p) \neq b.$$

$$(58) \quad \text{If } a:=\text{len } f \in \text{rng } p \text{ or } f := \underbrace{\langle 0, \dots, 0 \rangle}_a \in \text{rng } p, \text{ then } \text{FirstNotUsed}(p) \neq a.$$

## 6. CHOOSING A FINITE SEQUENCE LOCATION NOT USED IN A PROGRAM

In the sequel  $L$  is a finite subset of FinSeq-Locations.

Let  $L$  be a finite subset of FinSeq-Locations. The functor  $\text{First}^* \text{NotIn}(L)$  yielding a finite sequence location is defined by:

- (Def. 8) There exists a non empty subset  $s_1$  of  $\mathbb{N}$  such that  $\text{First}^* \text{NotIn}(L) = \text{fsloc}(\min s_1)$  and  $s_1 = \{k : k \text{ ranges over natural numbers, } \text{fsloc}(k) \notin L\}$ .

We now state two propositions:

- (59)  $\text{First}^* \text{NotIn}(L) \notin L$ .  
(60) If  $\text{First}^* \text{NotIn}(L) = \text{fsloc}(m)$  and  $\text{fsloc}(n) \notin L$ , then  $m \leq n$ .

Let  $p$  be a programmed finite partial state of  $\mathbf{SCM}_{\text{FSA}}$ . The functor  $\text{First}^* \text{NotUsed}(p)$  yields a finite sequence location and is defined by:

- (Def. 9) There exists a finite subset  $s_2$  of FinSeq-Locations such that  $s_2 = \text{UsedInt}^* \text{Loc}(p)$  and  $\text{First}^* \text{NotUsed}(p) = \text{First}^* \text{NotIn}(s_2)$ .

One can prove the following propositions:

- (61)  $\text{First}^* \text{NotUsed}(p) \notin \text{UsedInt}^* \text{Loc}(p)$ .  
(62) If  $b := f_a \in \text{rng } p$  or  $f_a := b \in \text{rng } p$ , then  $\text{First}^* \text{NotUsed}(p) \neq f$ .  
(63) If  $a := \text{len } f \in \text{rng } p$  or  $f := \underbrace{\langle 0, \dots, 0 \rangle}_a \in \text{rng } p$ , then  $\text{First}^* \text{NotUsed}(p) \neq f$ .

## 7. SEMANTICS

In the sequel  $s, t$  will be states of  $\mathbf{SCM}_{\text{FSA}}$ .

We now state a number of propositions:

- (64)  $\text{dom } I \cap \text{dom } \text{Start-At}(\text{insloc}(n)) = \emptyset$ .  
(65)  $\mathbf{IC}_{\mathbf{SCM}_{\text{FSA}}} \in \text{dom}(I + \cdot \text{Start-At}(\text{insloc}(n)))$ .  
(66)  $(I + \cdot \text{Start-At}(\text{insloc}(n)))(\mathbf{IC}_{\mathbf{SCM}_{\text{FSA}}}) = \text{insloc}(n)$ .  
(67) If  $I + \cdot \text{Start-At}(\text{insloc}(n)) \subseteq s$ , then  $\mathbf{IC}_s = \text{insloc}(n)$ .  
(68) If  $c \notin \text{UsedIntLoc}(i)$ , then  $(\text{Exec}(i, s))(c) = s(c)$ .  
(69) If  $I + \cdot \text{Start-At}(\text{insloc}(0)) \subseteq s$  and for every  $m$  such that  $m < n$  holds  $\mathbf{IC}_{(\text{Computation}(s))(m)} \in \text{dom } I$  and  $a \notin \text{UsedIntLoc}(I)$ , then  $(\text{Computation}(s))(n)(a) = s(a)$ .  
(70) If  $f \notin \text{UsedInt}^* \text{Loc}(i)$ , then  $(\text{Exec}(i, s))(f) = s(f)$ .  
(71) If  $I + \cdot \text{Start-At}(\text{insloc}(0)) \subseteq s$  and for every  $m$  such that  $m < n$  holds  $\mathbf{IC}_{(\text{Computation}(s))(m)} \in \text{dom } I$  and  $f \notin \text{UsedInt}^* \text{Loc}(I)$ , then  $(\text{Computation}(s))(n)(f) = s(f)$ .  
(72) If  $s \upharpoonright \text{UsedIntLoc}(i) = t \upharpoonright \text{UsedIntLoc}(i)$  and  $s \upharpoonright \text{UsedInt}^* \text{Loc}(i) = t \upharpoonright \text{UsedInt}^* \text{Loc}(i)$  and  $\mathbf{IC}_s = \mathbf{IC}_t$ , then  $\mathbf{IC}_{\text{Exec}(i, s)} = \mathbf{IC}_{\text{Exec}(i, t)}$  and  $\text{Exec}(i, s) \upharpoonright \text{UsedIntLoc}(i) = \text{Exec}(i, t) \upharpoonright \text{UsedIntLoc}(i)$  and  $\text{Exec}(i, s) \upharpoonright \text{UsedInt}^* \text{Loc}(i) = \text{Exec}(i, t) \upharpoonright \text{UsedInt}^* \text{Loc}(i)$ .

- (73) Suppose  $I+\cdot\text{Start-At}(\text{insloc}(0)) \subseteq s$  and  $I+\cdot\text{Start-At}(\text{insloc}(0)) \subseteq t$  and  $s \upharpoonright \text{UsedIntLoc}(I) = t \upharpoonright \text{UsedIntLoc}(I)$  and  $s \upharpoonright \text{UsedInt}^*\text{Loc}(I) = t \upharpoonright \text{UsedInt}^*\text{Loc}(I)$  and for every  $m$  such that  $m < n$  holds  $\mathbf{IC}_{(\text{Computation}(s))(m)} \in \text{dom } I$ . Then
- (i) for every  $m$  such that  $m < n$  holds  $\mathbf{IC}_{(\text{Computation}(t))(m)} \in \text{dom } I$ , and
  - (ii) for every  $m$  such that  $m \leq n$  holds  $\mathbf{IC}_{(\text{Computation}(s))(m)} = \mathbf{IC}_{(\text{Computation}(t))(m)}$  and for every  $a$  such that  $a \in \text{UsedIntLoc}(I)$  holds  $(\text{Computation}(s))(m)(a) = (\text{Computation}(t))(m)(a)$  and for every  $f$  such that  $f \in \text{UsedInt}^*\text{Loc}(I)$  holds  $(\text{Computation}(s))(m)(f) = (\text{Computation}(t))(m)(f)$ .

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