

# Another times Macro Instruction

Piotr Rudnicki<sup>1</sup>  
University of Alberta  
Edmonton

**Summary.** The semantics of the `times` macro is given in [2] only for the case when the body of the macro is parahalting. We remedy this by defining a new `times` macro instruction in terms of `while` (see [9, 13]). The semantics of the new `times` macro is given in a way analogous to the semantics of `while` macros. The new `times` uses an anonymous variable to control the number of its executions. We present two examples: a trivial one and a remake of the macro for the Fibonacci sequence (see [12]).

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The terminology and notation used in this paper are introduced in the following articles: [11], [16], [21], [6], [8], [19], [5], [7], [10], [22], [3], [4], [1], [18], [17], [12], [14], [20], and [15].

## 1. $\mathbf{SCM}_{\text{FSA}}$ PRELIMINARIES

For simplicity, we follow the rules:  $s, s_1, s_2$  denote states of  $\mathbf{SCM}_{\text{FSA}}$ ,  $a, b$  denote integer locations,  $d$  denotes a read-write integer location,  $f$  denotes a finite sequence location,  $I$  denotes a macro instruction,  $J$  denotes a good macro instruction, and  $k$  denotes a natural number.

One can prove the following propositions:

- (1) If  $I$  is closed on  $\text{Initialize}(s)$  and halting on  $\text{Initialize}(s)$  and  $b \notin \text{UsedIntLoc}(I)$ , then  $(\text{IExec}(I, s))(b) = (\text{Initialize}(s))(b)$ .
- (2) If  $I$  is closed on  $\text{Initialize}(s)$  and halting on  $\text{Initialize}(s)$  and  $f \notin \text{UsedInt}^* \text{Loc}(I)$ , then  $(\text{IExec}(I, s))(f) = (\text{Initialize}(s))(f)$ .

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- (3) Suppose  $I$  is closed on  $\text{Initialize}(s)$ , halting on  $\text{Initialize}(s)$ , and parahalting but  $s(\text{intloc}(0)) = 1$  or  $a$  is read-write but  $a \notin \text{UsedIntLoc}(I)$ . Then  $(\text{IExec}(I, s))(a) = s(a)$ .
- (4) If  $s(\text{intloc}(0)) = 1$ , then  $I$  is closed on  $s$  iff  $I$  is closed on  $\text{Initialize}(s)$ .
- (5) If  $s(\text{intloc}(0)) = 1$ , then  $I$  is closed on  $s$  and halting on  $s$  iff  $I$  is closed on  $\text{Initialize}(s)$  and halting on  $\text{Initialize}(s)$ .
- (6) Let  $I_1$  be a subset of Int-Locations and  $F_1$  be a subset of FinSeq-Locations. Then  $s_1 \upharpoonright (I_1 \cup F_1) = s_2 \upharpoonright (I_1 \cup F_1)$  if and only if the following conditions are satisfied:
  - (i) for every integer location  $x$  such that  $x \in I_1$  holds  $s_1(x) = s_2(x)$ , and
  - (ii) for every finite sequence location  $x$  such that  $x \in F_1$  holds  $s_1(x) = s_2(x)$ .
- (7) Let  $I_1$  be a subset of Int-Locations. Then  $s_1 \upharpoonright (I_1 \cup \text{FinSeq-Locations}) = s_2 \upharpoonright (I_1 \cup \text{FinSeq-Locations})$  if and only if the following conditions are satisfied:
  - (i) for every integer location  $x$  such that  $x \in I_1$  holds  $s_1(x) = s_2(x)$ , and
  - (ii) for every finite sequence location  $x$  holds  $s_1(x) = s_2(x)$ .

## 2. ANOTHER `times` MACRO INSTRUCTION

Let  $a$  be an integer location and let  $I$  be a macro instruction. The functor  $\text{times}(a, I)$  yields a macro instruction and is defined by:

(Def. 1)  $\text{times}(a, I) = (a_1 := a); (\text{while } a_1 > 0 \text{ do } (I; \text{SubFrom}(a_1, \text{intloc}(0))))$ ,  
 where  $a_1 = 1^{\text{st}}\text{-RWNotIn}(\{a\} \cup \text{UsedIntLoc}(I))$ .

We introduce  $a$  times  $I$  as a synonym of  $\text{times}(a, I)$ .

Next we state two propositions:

- (8)  $\{b\} \cup \text{UsedIntLoc}(I) \subseteq \text{UsedIntLoc}(\text{times}(b, I))$ .
- (9)  $\text{UsedInt}^* \text{Loc}(\text{times}(b, I)) = \text{UsedInt}^* \text{Loc}(I)$ .

Let  $I$  be a good macro instruction and let  $a$  be an integer location. Observe that  $\text{times}(a, I)$  is good.

Let  $s$  be a state of  $\mathbf{SCM}_{\text{FSA}}$ , let  $I$  be a macro instruction, and let  $a$  be an integer location. The functor  $\text{StepTimes}(a, I, s)$  yields a function from  $\mathbb{N}$  into  $\prod$  (the object kind of  $\mathbf{SCM}_{\text{FSA}}$ ) and is defined by:

(Def. 2)  $\text{StepTimes}(a, I, s) = \text{StepWhile} > 0(a_1, I; \text{SubFrom}(a_1, \text{intloc}(0)), \text{Exec}(a_1 := a, \text{Initialize}(s)))$ ,  
 where  $a_1 = 1^{\text{st}}\text{-RWNotIn}(\{a\} \cup \text{UsedIntLoc}(I))$ .

Next we state several propositions:

- (10)  $(\text{StepTimes}(a, J, s))(0)(\text{intloc}(0)) = 1$ .

- (11) If  $s(\text{intloc}(0)) = 1$  or  $a$  is read-write, then  $(\text{StepTimes}(a, J, s))$   
 $(0)(1^{\text{st}}\text{-RWNotIn}(\{a\} \cup \text{UsedIntLoc}(J))) = s(a)$ .
- (12) Suppose  $(\text{StepTimes}(a, J, s))(k)(\text{intloc}(0)) = 1$  and  $J$  is closed on  
 $(\text{StepTimes}(a, J, s))(k)$  and halting on  $(\text{StepTimes}(a, J, s))(k)$ . Then  
 $(\text{StepTimes}(a, J, s))(k+1)(\text{intloc}(0)) = 1$  and if  $(\text{StepTimes}(a, J, s))(k)$   
 $(1^{\text{st}}\text{-RWNotIn}(\{a\} \cup \text{UsedIntLoc}(J))) > 0$ , then  $(\text{StepTimes}(a, J, s))(k+1)$   
 $(1^{\text{st}}\text{-RWNotIn}(\{a\} \cup \text{UsedIntLoc}(J))) = (\text{StepTimes}(a, J, s))(k)$   
 $(1^{\text{st}}\text{-RWNotIn}(\{a\} \cup \text{UsedIntLoc}(J))) - 1$ .
- (13) If  $s(\text{intloc}(0)) = 1$  or  $a$  is read-write, then  $(\text{StepTimes}(a, I, s))(0)(a) =$   
 $s(a)$ .
- (14)  $(\text{StepTimes}(a, I, s))(0)(f) = s(f)$ .

Let  $s$  be a state of  $\mathbf{SCM}_{\text{FSA}}$ , let  $a$  be an integer location, and let  $I$  be a macro instruction. We say that  $\text{ProperTimesBody } a, I, s$  if and only if:

- (Def. 3) For every natural number  $k$  such that  $k < s(a)$  holds  $I$  is closed on  
 $(\text{StepTimes}(a, I, s))(k)$  and halting on  $(\text{StepTimes}(a, I, s))(k)$ .

One can prove the following propositions:

- (15) If  $I$  is parahalting, then  $\text{ProperTimesBody } a, I, s$ .
- (16) If  $\text{ProperTimesBody } a, J, s$ , then for every  $k$  such that  $k \leq s(a)$  holds  
 $(\text{StepTimes}(a, J, s))(k)(\text{intloc}(0)) = 1$ .
- (17) Suppose  $s(\text{intloc}(0)) = 1$  or  $a$  is read-write but  $\text{ProperTimesBody } a, J, s$ .  
Let given  $k$ . If  $k \leq s(a)$ , then  $(\text{StepTimes}(a, J, s))(k)(1^{\text{st}}\text{-RWNotIn}(\{a\} \cup$   
 $\text{UsedIntLoc}(J))) + k = s(a)$ .
- (18) Suppose  $\text{ProperTimesBody } a, J, s$  but  $0 \leq s(a)$  but  $s(\text{intloc}(0)) = 1$  or  
 $a$  is read-write. Let given  $k$ . If  $k \geq s(a)$ , then  $(\text{StepTimes}(a, J, s))(k)$   
 $(1^{\text{st}}\text{-RWNotIn}(\{a\} \cup \text{UsedIntLoc}(J))) = 0$  and  $(\text{StepTimes}(a, J, s))$   
 $(k)(\text{intloc}(0)) = 1$ .
- (19) If  $s(\text{intloc}(0)) = 1$ , then  $(\text{StepTimes}(a, I, s))(0) \upharpoonright (\text{UsedIntLoc}(I) \cup$   
 $\text{FinSeq-Locations}) = s \upharpoonright (\text{UsedIntLoc}(I) \cup \text{FinSeq-Locations})$ .
- (20) Suppose  $(\text{StepTimes}(a, J, s))(k)(\text{intloc}(0)) = 1$  and  $J$  is halting on  
 $\text{Initialize}((\text{StepTimes}(a, J, s))(k))$  and closed on  $\text{Initialize}((\text{StepTimes}(a, J, s))$   
 $(k))$  and  $(\text{StepTimes}(a, J, s))(k)(1^{\text{st}}\text{-RWNotIn}(\{a\} \cup \text{UsedIntLoc}(J))) > 0$ .  
Then  $(\text{StepTimes}(a, J, s))(k+1) \upharpoonright (\text{UsedIntLoc}(J) \cup \text{FinSeq-Locations}) =$   
 $\text{IExec}(J, (\text{StepTimes}(a, J, s))(k)) \upharpoonright (\text{UsedIntLoc}(J) \cup \text{FinSeq-Locations})$ .
- (21) Suppose  $\text{ProperTimesBody } a, J, s$  or  $J$  is parahalting but  
 $k < s(a)$  but  $s(\text{intloc}(0)) = 1$  or  $a$  is read-write. Then  
 $(\text{StepTimes}(a, J, s))(k+1) \upharpoonright (\text{UsedIntLoc}(J) \cup \text{FinSeq-Locations}) =$   
 $\text{IExec}(J, (\text{StepTimes}(a, J, s))(k)) \upharpoonright (\text{UsedIntLoc}(J) \cup \text{FinSeq-Locations})$ .
- (22) If  $s(a) \leq 0$  and  $s(\text{intloc}(0)) = 1$ , then  $\text{IExec}(\text{times}(a, I), s) \upharpoonright (\text{UsedIntLoc}(I) \cup$   
 $\text{FinSeq-Locations}) = s \upharpoonright (\text{UsedIntLoc}(I) \cup \text{FinSeq-Locations})$ .

- (23) Suppose  $s(a) = k$  but ProperTimesBody  $a, J, s$  or  $J$  is parahalting but  $s(\text{intloc}(0)) = 1$  or  $a$  is read-write. Then  $\text{IExec}(\text{times}(a, J), s) \upharpoonright D = (\text{StepTimes}(a, J, s))(k) \upharpoonright D$ , where  $D = \text{Int-Locations} \cup \text{FinSeq-Locations}$ .
- (24) If  $s(\text{intloc}(0)) = 1$  and if ProperTimesBody  $a, J, s$  or  $J$  is parahalting, then  $\text{times}(a, J)$  is closed on  $s$  and  $\text{times}(a, J)$  is halting on  $s$ .

### 3. A TRIVIAL EXAMPLE

Let  $d$  be a read-write integer location. The functor  $\text{triv-times}(d)$  yields a macro instruction and is defined as follows:

- (Def. 4)  $\text{triv-times}(d) =$   
 $\text{times}(d, (\mathbf{while} \ d = 0 \ \mathbf{do} \ \text{Macro}(d:=d));$   
 $\text{SubFrom}(d, \text{intloc}(0))).$

One can prove the following propositions:

- (25) If  $s(d) \leq 0$ , then  $(\text{IExec}(\text{triv-times}(d), s))(d) = s(d)$ .
- (26) If  $0 \leq s(d)$ , then  $(\text{IExec}(\text{triv-times}(d), s))(d) = 0$ .

### 4. A MACRO FOR THE FIBONACCI SEQUENCE

Let  $N, r_1$  be integer locations. The functor  $\text{Fib-macro}(N, r_1)$  yields a macro instruction and is defined by:

- (Def. 5)  $\text{Fib-macro}(N, r_1) =$   
 $(N_1:=N);$   
 $\text{SubFrom}(r_1, r_1);$   
 $(n_1:=\text{intloc}(0));$   
 $\text{times}(N, \text{AddTo}(r_1, n_1); \text{swap}(r_1, n_1));$   
 $(N:=N_1),$   
 where  $N_1 = 1^{\text{st}}\text{-NotUsed}(\text{times}(N, \text{AddTo}(r_1, n_1); \text{swap}(r_1, n_1)))$  and  $n_1 = 1^{\text{st}}\text{-RWNotIn}(\{N, r_1\})$ .

One can prove the following proposition

- (27) Let  $N, r_1$  be read-write integer locations. Suppose  $N \neq r_1$ . Let  $n$  be a natural number. If  $n = s(N)$ , then  $(\text{IExec}(\text{Fib-macro}(N, r_1), s))(r_1) = \text{Fib}(n)$  and  $(\text{IExec}(\text{Fib-macro}(N, r_1), s))(N) = s(N)$ .

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