

Computation of Two Consecutive Program Blocks for SCMPDS¹

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Summary. In this article, a program block without halting instructions is called No-StopCode program block. If a program consists of two blocks, where the first block is parahalting (i.e. halt for all states) and No-StopCode, and the second block is parahalting and shiftable, it can be computed by combining the computation results of the two blocks. For a program which consists of a instruction and a block, we obtain a similar conclusion. For a large amount of programs, the computation method given in the article is useful, but it is not suitable to recursive programs.

MML Identifier: SCMPDS_5.

The terminology and notation used here have been introduced in the following articles: [16], [20], [11], [21], [5], [6], [18], [2], [12], [13], [17], [14], [4], [10], [9], [19], [7], [1], [15], [8], and [3].

1. PRELIMINARIES

For simplicity, we use the following convention: x denotes a set, m, n denote natural numbers, a, b denote Int position, i denotes an instruction of SCMPDS, s, s_1, s_2 denote states of SCMPDS, k_1, k_2 denote integers, l_1 denotes an instruction-location of SCMPDS, I, J denote Program-block, and N denotes a set with non empty elements.

One can prove the following propositions:

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- (1) Let S be a halting von Neumann definite AMI over N and s be a state of S . If $s = \text{Following}(s)$, then for every n holds $(\text{Computation}(s))(n) = s$.
- (2) $x \in \text{dom Load}(i)$ iff $x = \text{inspos } 0$.
- (3) If $l_1 \in \text{dom stop } I$ and $(\text{stop } I)(l_1) \neq \mathbf{halt}_{\text{SCMPDS}}$, then $l_1 \in \text{dom } I$.
- (4) $\text{dom Load}(i) = \{\text{inspos } 0\}$ and $(\text{Load}(i))(\text{inspos } 0) = i$.
- (5) $\text{inspos } 0 \in \text{dom Load}(i)$.
- (6) $\text{card Load}(i) = 1$.
- (7) $\text{card stop } I = \text{card } I + 1$.
- (8) $\text{card stop Load}(i) = 2$.
- (9) $\text{inspos } 0 \in \text{dom stop Load}(i)$ and $\text{inspos } 1 \in \text{dom stop Load}(i)$.
- (10) $(\text{stop Load}(i))(\text{inspos } 0) = i$ and $(\text{stop Load}(i))(\text{inspos } 1) = \mathbf{halt}_{\text{SCMPDS}}$.
- (11) $x \in \text{dom stop Load}(i)$ iff $x = \text{inspos } 0$ or $x = \text{inspos } 1$.
- (12) $\text{dom stop Load}(i) = \{\text{inspos } 0, \text{inspos } 1\}$.
- (13) $\text{inspos } 0 \in \text{dom Initialized}(\text{stop Load}(i))$ and $\text{inspos } 1 \in \text{dom Initialized}(\text{stop Load}(i))$ and $(\text{Initialized}(\text{stop Load}(i)))(\text{inspos } 0) = i$ and $(\text{Initialized}(\text{stop Load}(i)))(\text{inspos } 1) = \mathbf{halt}_{\text{SCMPDS}}$.
- (14) For all Program-block I, J holds $\text{Initialized}(\text{stop } I; J) = (I; (J; \text{SCMPDS} - \text{Stop})) + \cdot \text{Start-At}(\text{inspos } 0)$.
- (15) For all Program-block I, J holds $\text{Initialized}(I) \subseteq \text{Initialized}(\text{stop } I; J)$.
- (16) $\text{dom stop } I \subseteq \text{dom stop } I; J$.
- (17) For all Program-block I, J holds $\text{Initialized}(\text{stop } I) + \cdot \text{Initialized}(\text{stop } I; J) = \text{Initialized}(\text{stop } I; J)$.
- (18) If $\text{Initialized}(I) \subseteq s$, then $\mathbf{IC}_s = \text{inspos } 0$.
- (19) $(s + \cdot \text{Initialized}(I))(a) = s(a)$.
- (20) Let I be a parahalting Program-block. Suppose $\text{Initialized}(\text{stop } I) \subseteq s_1$ and $\text{Initialized}(\text{stop } I) \subseteq s_2$ and s_1 and s_2 are equal outside the instruction locations of SCMPDS. Let k be a natural number. Then $(\text{Computation}(s_1))(k)$ and $(\text{Computation}(s_2))(k)$ are equal outside the instruction locations of SCMPDS and $\text{CurInstr}((\text{Computation}(s_1))(k)) = \text{CurInstr}((\text{Computation}(s_2))(k))$.
- (21) Let I be a parahalting Program-block. Suppose $\text{Initialized}(\text{stop } I) \subseteq s_1$ and $\text{Initialized}(\text{stop } I) \subseteq s_2$ and s_1 and s_2 are equal outside the instruction locations of SCMPDS. Then $\text{LifeSpan}(s_1) = \text{LifeSpan}(s_2)$ and $\text{Result}(s_1)$ and $\text{Result}(s_2)$ are equal outside the instruction locations of SCMPDS.
- (22) For every Program-block I holds $\mathbf{IC}_{\text{IExec}(I, s)} = \mathbf{IC}_{\text{Result}(s + \cdot \text{Initialized}(\text{stop } I))}$.
- (23) Let I be a parahalting Program-block and J be a Program-block. Suppose $\text{Initialized}(\text{stop } I) \subseteq s$. Let given m . Suppose $m \leq \text{LifeSpan}(s)$. Then $(\text{Computation}(s))(m)$ and $(\text{Computation}(s + \cdot (I; J)))(m)$ are equal outside the instruction locations of SCMPDS.

- (24) Let I be a parahalting Program-block and J be a Program-block. Suppose $\text{Initialized}(\text{stop } I) \subseteq s$. Let given m . Suppose $m \leq \text{LifeSpan}(s)$. Then $(\text{Computation}(s))(m)$ and $(\text{Computation}(s + \cdot \text{Initialized}(\text{stop } I; J)))(m)$ are equal outside the instruction locations of SCMPDS.

2. NON HALTING INSTRUCTIONS AND PARAHALTING INSTRUCTIONS

Let i be an instruction of SCMPDS. We say that i is No-StopCode if and only if:

(Def. 1) $i \neq \mathbf{halt}_{\text{SCMPDS}}$.

Let i be an instruction of SCMPDS. We say that i is parahalting if and only if:

(Def. 2) $\text{Load}(i)$ is parahalting.

One can verify that there exists an instruction of SCMPDS which is No-StopCode, shiftable, and parahalting.

One can prove the following proposition

- (25) If $k_1 \neq 0$, then $\text{goto } k_1$ is No-StopCode.

Let us consider a . Observe that $\text{return } a$ is No-StopCode.

Let us consider a, k_1 . Note that $a := k_1$ is No-StopCode and $\text{saveIC}(a, k_1)$ is No-StopCode.

Let us consider a, k_1, k_2 . One can check the following observations:

- * $(a, k_1) <> 0_gotok_2$ is No-StopCode,
- * $(a, k_1) \leq 0_gotok_2$ is No-StopCode,
- * $(a, k_1) \geq 0_gotok_2$ is No-StopCode, and
- * $a_{k_1} := k_2$ is No-StopCode.

Let us consider a, k_1, k_2 . Note that $\text{AddTo}(a, k_1, k_2)$ is No-StopCode.

Let us consider a, b, k_1, k_2 . One can verify the following observations:

- * $\text{AddTo}(a, k_1, b, k_2)$ is No-StopCode,
- * $\text{SubFrom}(a, k_1, b, k_2)$ is No-StopCode,
- * $\text{MultBy}(a, k_1, b, k_2)$ is No-StopCode,
- * $\text{Divide}(a, k_1, b, k_2)$ is No-StopCode, and
- * $(a, k_1) := (b, k_2)$ is No-StopCode.

Let us note that $\mathbf{halt}_{\text{SCMPDS}}$ is parahalting.

Let i be a parahalting instruction of SCMPDS. Observe that $\text{Load}(i)$ is parahalting.

Let us consider a, k_1 . Observe that $a := k_1$ is parahalting.

Let us consider a, k_1, k_2 . Note that $a_{k_1} := k_2$ is parahalting and $\text{AddTo}(a, k_1, k_2)$ is parahalting.

Let us consider a, b, k_1, k_2 . One can check the following observations:

- * $\text{AddTo}(a, k_1, b, k_2)$ is parahalting,
- * $\text{SubFrom}(a, k_1, b, k_2)$ is parahalting,
- * $\text{MultBy}(a, k_1, b, k_2)$ is parahalting,
- * $\text{Divide}(a, k_1, b, k_2)$ is parahalting, and
- * $(a, k_1) := (b, k_2)$ is parahalting.

Next we state the proposition

- (26) If $\text{InsCode}(i) = 1$, then i is not parahalting.

Let I_1 be a finite partial state of SCMPDS. We say that I_1 is No-StopCode if and only if:

- (Def. 3) For every instruction-location x of SCMPDS such that $x \in \text{dom } I_1$ holds $I_1(x) \neq \mathbf{halt}_{\text{SCMPDS}}$.

Let us observe that there exists a Program-block which is parahalting, shiftable, and No-StopCode.

Let I, J be No-StopCode Program-block. Observe that $I;J$ is No-StopCode.

Let i be a No-StopCode instruction of SCMPDS. Observe that $\text{Load}(i)$ is No-StopCode.

Let i be a No-StopCode instruction of SCMPDS and let J be a No-StopCode Program-block. Note that $i;J$ is No-StopCode.

Let I be a No-StopCode Program-block and let j be a No-StopCode instruction of SCMPDS. Observe that $I;j$ is No-StopCode.

Let i, j be No-StopCode instructions of SCMPDS. Observe that $i;j$ is No-StopCode.

Next we state several propositions:

- (27) For every parahalting No-StopCode Program-block I such that $\text{Initialized}(\text{stop } I) \subseteq s$ holds $\mathbf{IC}_{(\text{Computation}(s))(\text{LifeSpan}(s+\cdot \text{Initialized}(\text{stop } I)))} = \text{inspos card } I$.
- (28) For every parahalting Program-block I and for every natural number k such that $k < \text{LifeSpan}(s+\cdot \text{Initialized}(\text{stop } I))$ holds $\mathbf{IC}_{(\text{Computation}(s+\cdot \text{Initialized}(\text{stop } I)))(k)} \in \text{dom } I$.
- (29) Let I be a parahalting Program-block and k be a natural number. Suppose $\text{Initialized}(I) \subseteq s$ and $k \leq \text{LifeSpan}(s+\cdot \text{Initialized}(\text{stop } I))$. Then $(\text{Computation}(s))(k)$ and $(\text{Computation}(s+\cdot \text{Initialized}(\text{stop } I)))(k)$ are equal outside the instruction locations of SCMPDS.
- (30) For every parahalting No-StopCode Program-block I such that $\text{Initialized}(I) \subseteq s$ holds $\mathbf{IC}_{(\text{Computation}(s))(\text{LifeSpan}(s+\cdot \text{Initialized}(\text{stop } I)))} = \text{inspos card } I$.
- (31) For every parahalting Program-block I such that $\text{Initialized}(I) \subseteq s$ holds $\text{CurInstr}((\text{Computation}(s))(\text{LifeSpan}(s+\cdot \text{Initialized}(\text{stop } I)))) = \mathbf{halt}_{\text{SCMPDS}}$ or $\mathbf{IC}_{(\text{Computation}(s))(\text{LifeSpan}(s+\cdot \text{Initialized}(\text{stop } I)))} = \text{inspos card } I$.

- (32) Let I be a parahalting No-StopCode Program-block and k be a natural number. If $\text{Initialized}(I) \subseteq s$ and $k < \text{LifeSpan}(s+\cdot\text{Initialized}(\text{stop } I))$, then $\text{CurInstr}((\text{Computation}(s))(k)) \neq \mathbf{halt}_{\text{SCMPDS}}$.
- (33) Let I be a parahalting Program-block, J be a Program-block, and k be a natural number. Suppose $k \leq \text{LifeSpan}(s+\cdot\text{Initialized}(\text{stop } I))$. Then $(\text{Computation}(s+\cdot\text{Initialized}(\text{stop } I)))(k)$ and $(\text{Computation}(s+\cdot((I;J)+\cdot\text{Start-At}(\text{inspos}0))))(k)$ are equal outside the instruction locations of SCMPDS.
- (34) Let I be a parahalting Program-block, J be a Program-block, and k be a natural number. Suppose $k \leq \text{LifeSpan}(s+\cdot\text{Initialized}(\text{stop } I))$. Then $(\text{Computation}(s+\cdot\text{Initialized}(\text{stop } I)))(k)$ and $(\text{Computation}(s+\cdot\text{Initialized}(\text{stop } I;J)))(k)$ are equal outside the instruction locations of SCMPDS.

Let I be a parahalting Program-block and let J be a parahalting shiftable Program-block. One can verify that $I;J$ is parahalting.

Let i be a parahalting instruction of SCMPDS and let J be a parahalting shiftable Program-block. Note that $i;J$ is parahalting.

Let I be a parahalting Program-block and let j be a parahalting shiftable instruction of SCMPDS. Observe that $I;j$ is parahalting.

Let i be a parahalting instruction of SCMPDS and let j be a parahalting shiftable instruction of SCMPDS. One can check that $i;j$ is parahalting.

Next we state the proposition

- (35) Let s, s_1 be states of SCMPDS and J be a parahalting shiftable Program-block. If $s = (\text{Computation}(s_1+\cdot\text{Initialized}(\text{stop } J)))(m)$, then $\text{Exec}(\text{CurInstr}(s), s+\cdot\text{Start-At}(\mathbf{IC}_s + n)) = \text{Following}(s)+\cdot\text{Start-At}(\mathbf{IC}_{\text{Following}(s)} + n)$.

3. COMPUTATION OF TWO CONSECUTIVE PROGRAM BLOCKS

The following propositions are true:

- (36) Let I be a parahalting No-StopCode Program-block, J be a parahalting shiftable Program-block, and k be a natural number. Suppose $\text{Initialized}(\text{stop } I;J) \subseteq s$. Then $(\text{Computation}(\text{Result}(s+\cdot\text{Initialized}(\text{stop } I))+\cdot\text{Initialized}(\text{stop } J)))(k)+\cdot\text{Start-At}(\mathbf{IC}_{(\text{Computation}(\text{Result}(s+\cdot\text{Initialized}(\text{stop } I))+\cdot\text{Initialized}(\text{stop } J)))(k)} + \text{card } I)$ and $(\text{Computation}(s+\cdot\text{Initialized}(\text{stop } I;J)))(\text{LifeSpan}(s+\cdot\text{Initialized}(\text{stop } I))+k)$ are equal outside the instruction locations of SCMPDS.
- (37) Let I be a parahalting No-StopCode Program-block and J be a parahalting shiftable Program-block. Then $\text{LifeSpan}(s+\cdot\text{Initialized}(\text{stop } I;J)) = \text{LifeSpan}(s+\cdot\text{Initialized}(\text{stop } I))+\text{LifeSpan}(\text{Result}(s+\cdot\text{Initialized}(\text{stop } I))+\cdot\text{Initialized}(\text{stop } J))$.

- (38) Let I be a parahalting No-StopCode Program-block and J be a parahalting shiftable Program-block. Then $\text{IExec}(I;J,s) = \text{IExec}(J, \text{IExec}(I,s)) + \cdot \text{Start-At}(\mathbf{IC}_{\text{IExec}(J, \text{IExec}(I,s))} + \text{card } I)$.
- (39) Let I be a parahalting No-StopCode Program-block and J be a parahalting shiftable Program-block. Then $(\text{IExec}(I;J,s))(a) = (\text{IExec}(J, \text{IExec}(I,s)))(a)$.

4. COMPUTATION OF THE PROGRAM CONSISTING OF A INSTRUCTION AND A BLOCK

Let s be a state of SCMPDS. The functor $\text{Initialized}(s)$ yields a state of SCMPDS and is defined by:

(Def. 4) $\text{Initialized}(s) = s + \cdot \text{Start-At}(\text{inspos } 0)$.

Next we state several propositions:

- (40) $\mathbf{IC}_{\text{Initialized}(s)} = \text{inspos } 0$ and $(\text{Initialized}(s))(a) = s(a)$ and $(\text{Initialized}(s))(l_1) = s(l_1)$.
- (41) s_1 and s_2 are equal outside the instruction locations of SCMPDS iff $s_1 \upharpoonright (\text{Data-Loc}_{\text{SCM}} \cup \{\mathbf{IC}_{\text{SCMPDS}}\}) = s_2 \upharpoonright (\text{Data-Loc}_{\text{SCM}} \cup \{\mathbf{IC}_{\text{SCMPDS}}\})$.
- (42) If $s_1 \upharpoonright \text{Data-Loc}_{\text{SCM}} = s_2 \upharpoonright \text{Data-Loc}_{\text{SCM}}$, then $s_1(\text{DataLoc}(s_1(a), k_1)) = s_2(\text{DataLoc}(s_2(a), k_1))$.
- (43) If $s_1 \upharpoonright \text{Data-Loc}_{\text{SCM}} = s_2 \upharpoonright \text{Data-Loc}_{\text{SCM}}$ and $\text{InsCode}(i) \neq 3$, then $\text{Exec}(i, s_1) \upharpoonright \text{Data-Loc}_{\text{SCM}} = \text{Exec}(i, s_2) \upharpoonright \text{Data-Loc}_{\text{SCM}}$.
- (44) For every shiftable instruction i of SCMPDS such that $s_1 \upharpoonright \text{Data-Loc}_{\text{SCM}} = s_2 \upharpoonright \text{Data-Loc}_{\text{SCM}}$ holds $\text{Exec}(i, s_1) \upharpoonright \text{Data-Loc}_{\text{SCM}} = \text{Exec}(i, s_2) \upharpoonright \text{Data-Loc}_{\text{SCM}}$.
- (45) For every parahalting instruction i of SCMPDS holds $\text{Exec}(i, \text{Initialized}(s)) = \text{IExec}(\text{Load}(i), s)$.
- (46) Let I be a parahalting No-StopCode Program-block and j be a parahalting shiftable instruction of SCMPDS. Then $(\text{IExec}(I; j, s))(a) = (\text{Exec}(j, \text{IExec}(I, s)))(a)$.
- (47) Let i be a No-StopCode parahalting instruction of SCMPDS and j be a shiftable parahalting instruction of SCMPDS. Then $(\text{IExec}(i; j, s))(a) = (\text{Exec}(j, \text{Exec}(i, \text{Initialized}(s))))(a)$.

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