A Tree of Execution of a Macroinstruction¹

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Summary. A tree of execution of a macroinstruction is defined. It is a tree decorated by the instruction locations of a computer. Successors of each vertex are determined by the set of all possible values of the instruction counter after execution of the instruction placed in the location indicated by given vertex.

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The articles [22], [14], [25], [15], [1], [20], [3], [4], [16], [26], [11], [13], [12], [5], [6], [21], [9], [8], [10], [2], [7], [18], [23], [19], [24], and [17] provide the notation and terminology for this paper.

For simplicity, we adopt the following convention: x, y, X are sets, m, n are natural numbers, O is an ordinal number, and R, S are binary relations.

Let D be a set, let f be a partial function from D to N, and let n be a set. One can verify that f(n) is natural.

Let R be an empty binary relation and let X be a set. Observe that $R \upharpoonright X$ is empty.

One can prove the following two propositions:

(1) If dom $R = \{x\}$ and rng $R = \{y\}$, then $R = x \mapsto y$.

(2) field $\{\langle x, x \rangle\} = \{x\}.$

Let X be an infinite set and let a be a set. One can verify that $X \longmapsto a$ is infinite.

One can check that there exists a function which is infinite.

Let R be a finite binary relation. One can verify that field R is finite.

The following proposition is true

(3) If field R is finite, then R is finite.

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Let R be an infinite binary relation. Note that field R is infinite. One can prove the following proposition

(4) If dom R is finite and rng R is finite, then R is finite.

Let us observe that \subseteq_{\emptyset} is empty.

Let X be a non empty set. One can verify that \subseteq_X is non empty. Next we state two propositions:

- (5) $\subseteq_{\{x\}} = \{\langle x, x \rangle\}.$
- $(6) \quad {}^{\subseteq}_X \subseteq [X, X].$

Let X be a finite set. Note that \subseteq_X is finite.

One can prove the following proposition

(7) If \subseteq_X is finite, then X is finite.

Let X be an infinite set. One can verify that \subseteq_X is infinite.

The following propositions are true:

- (8) If R and S are isomorphic and R is well-ordering, then S is well-ordering.
- (9) If R and S are isomorphic and R is finite, then S is finite.
- (10) $x \mapsto y$ is an isomorphism between $\{\langle x, x \rangle\}$ and $\{\langle y, y \rangle\}$.
- (11) $\{\langle x, x \rangle\}$ and $\{\langle y, y \rangle\}$ are isomorphic.

One can verify that $\overline{\emptyset}$ is empty.

The following propositions are true:

- (12) $\subseteq_O = O.$
- (13) For every finite set X such that $X \subseteq O$ holds $\overline{\subseteq}_X = \operatorname{card} X$.
- (14) If $\{x\} \subseteq O$, then $\overline{\subseteq_{\{x\}}} = 1$.
- (15) If $\{x\} \subseteq O$, then the canonical isomorphism between $\subseteq_{\subseteq_{\{x\}}}$ and $\subseteq_{\{x\}} = 0 \mapsto x$.

Let O be an ordinal number, let X be a subset of O, and let n be a set. One can check that (the canonical isomorphism between $\subseteq_{\subseteq_{Y}}$ and \subseteq_{X})(n) is ordinal.

Let X be a natural-membered set and let n be a set. Note that (the canonical isomorphism between $\subseteq_{\overline{\subseteq_X}}$ and \subseteq_X)(n) is natural.

Next we state three propositions:

- (16) If $n \mapsto x = m \mapsto x$, then n = m.
- (17) For every tree T and for every element t of T holds $t \upharpoonright \text{Seg } n \in T$.
- (18) For all trees T_1 , T_2 such that for every natural number n holds T_1 -level $(n) = T_2$ -level(n) holds $T_1 = T_2$.

The functor TrivialInfiniteTree is defined by:

- (Def. 1) TrivialInfiniteTree = $\{k \mapsto 0 : k \text{ ranges over natural numbers}\}$. One can check that TrivialInfiniteTree is non empty and tree-like. We now state the proposition
 - (19) $\mathbb{N} \approx \text{TrivialInfiniteTree}.$

Let us note that TrivialInfiniteTree is infinite.

The following proposition is true

(20) For every natural number n holds TrivialInfiniteTree-level $(n) = \{n \mapsto 0\}$.

For simplicity, we adopt the following convention: N denotes a set with non empty elements, S denotes a standard IC-Ins-separated definite non empty non void AMI over N, L, l_1 denote instruction-locations of S, J denotes an instruction of S, and F denotes a subset of the instruction locations of S.

Let N be a set with non empty elements, let S be a standard IC-Ins-separated definite non empty non void AMI over N, and let F be a finite partial state of S. Let us assume that F is non empty and F is programmed. The functor FirstLoc(F) yields an instruction-location of S and is defined by the condition (Def. 2).

(Def. 2) There exists a non empty subset M of \mathbb{N} such that $M = \{\text{locnum}(l); l \text{ ranges over elements of the instruction locations of } S: l \in \text{dom } F\}$ and $\text{FirstLoc}(F) = \text{il}_S(\min M).$

One can prove the following four propositions:

- (21) For every non empty programmed finite partial state F of S holds $\operatorname{FirstLoc}(F) \in \operatorname{dom} F$.
- (22) For all non empty programmed finite partial states F, G of S such that $F \subseteq G$ holds $\operatorname{FirstLoc}(G) \leq \operatorname{FirstLoc}(F)$.
- (23) For every non empty programmed finite partial state F of S such that $l_1 \in \text{dom } F$ holds $\text{FirstLoc}(F) \leq l_1$.
- (24) For every lower non empty programmed finite partial state F of S holds FirstLoc(F) = $il_S(0)$.

Let N be a set with non empty elements, let S be a standard IC-Ins-separated definite non empty non void AMI over N, and let F be a subset of the instruction locations of S. The functor LocNums(F) yields a subset of \mathbb{N} and is defined by:

(Def. 3) LocNums $(F) = \{ \text{locnum}(l); l \text{ ranges over instruction-locations of } S: l \in F \}.$

We now state the proposition

(25) $\operatorname{locnum}(l_1) \in \operatorname{LocNums}(F)$ iff $l_1 \in F$.

Let N be a set with non empty elements, let S be a standard IC-Ins-separated definite non empty non void AMI over N, and let F be an empty subset of the instruction locations of S. Observe that LocNums(F) is empty.

Let N be a set with non empty elements, let S be a standard IC-Ins-separated definite non empty non void AMI over N, and let F be a non empty subset of the instruction locations of S. Observe that LocNums(F) is non empty.

We now state several propositions:

(26) If $F = \{ il_S(n) \}$, then LocNums $(F) = \{ n \}$.

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- (27) $F \approx \text{LocNums}(F)$.
- (28) $\overline{\overline{F}} \subseteq \overline{\subseteq}_{\operatorname{LocNums}(F)}.$
- (29) If S is realistic and J is halting, then $LocNums(NIC(J,L)) = \{locnum(L)\}.$
- (30) If S is realistic and J is sequential, then $LocNums(NIC(J,L)) = \{locnum(NextLoc L)\}.$

Let N be a set with non empty elements, let S be a standard IC-Ins-separated definite non empty non void AMI over N, and let M be a subset of the instruction locations of S. The functor LocSeq(M) yielding a transfinite sequence of elements of the instruction locations of S is defined as follows:

(Def. 4) dom LocSeq(M) = \overline{M} and for every set m such that $m \in \overline{M}$ holds (LocSeq(M))(m) = il_S((the canonical isomorphism between $\subseteq_{\underline{\subseteq}_{\text{LocNums}(M)}}$

and $\subseteq_{\operatorname{LocNums}(M)}(m)$).

One can prove the following proposition

(31) If $F = \{ il_S(n) \}$, then $LocSeq(F) = 0 \mapsto il_S(n)$.

Let N be a set with non empty elements, let S be a standard IC-Ins-separated definite non empty non void AMI over N, and let M be a subset of the instruction locations of S. Note that LocSeq(M) is one-to-one.

Let N be a set with non empty elements, let S be a standard IC-Ins-separated definite non empty non void AMI over N, and let M be a finite partial state of S. The functor ExecTree(M) yields a tree decorated with elements of the instruction locations of S and is defined by the conditions (Def. 5).

(Def. 5)(i) $(\text{ExecTree}(M))(\emptyset) = \text{FirstLoc}(M)$, and

(ii) for every element t of dom ExecTree(M) holds succ $t = \{t^{\land}\langle k\rangle; k \text{ ranges}$ over natural numbers: $k \in \overline{\operatorname{NIC}(\pi_{(\operatorname{ExecTree}(M))(t)}M, (\operatorname{ExecTree}(M))(t))}\}$ and for every natural number m such that

$$m \in \operatorname{NIC}(\pi_{(\operatorname{ExecTree}(M))(t)}M, (\operatorname{ExecTree}(M))(t)) \text{ holds } (\operatorname{ExecTree}(M))(t \cap \langle m \rangle) = (\operatorname{LocSeq}(\operatorname{NIC}(\pi_{(\operatorname{ExecTree}(M))(t)}M, (\operatorname{ExecTree}(M))(t))))(m).$$

One can prove the following proposition

(32) For every standard halting realistic IC-Ins-separated definite non empty non void AMI S over N holds ExecTree(Stop S) = TrivialInfiniteTree \mapsto $il_S(0)$.

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